

PATENT

IBM/273
Confirmation No. 4826

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I hereby certify that this correspondence for Application No.
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<u>/William R. Allen/</u>	<u>October 11, 2006</u>
William R. Allen	Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Furukawa et al.	Art Unit:	2814
Serial No.:	10/767,039	Examiner:	Phat X. Cao
Filed:	January 29, 2004	Atty. Docket No.:	ROC920030272US1
For:	VERTICAL FIELD EFFECT TRANSISTORS INCORPORATING SEMICONDUCTING NANOTUBES GROWN IN A SPACER-DEFINED PASSAGE		

AMENDMENT

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In reply to the Office Action dated July 13, 2006, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims that begins on page 2 of this paper.

Remarks begin on page 8 of this paper.